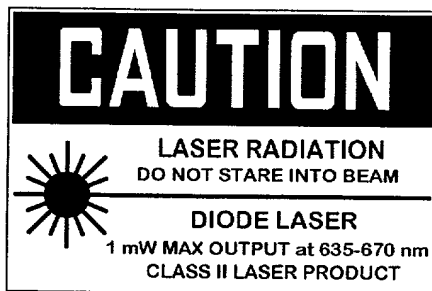


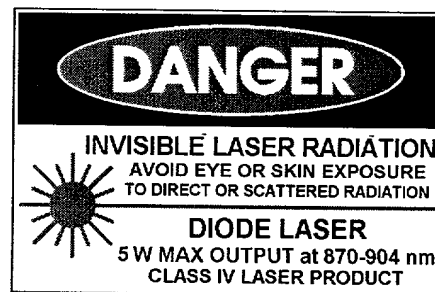
A



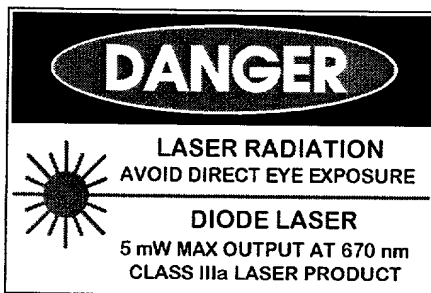
B



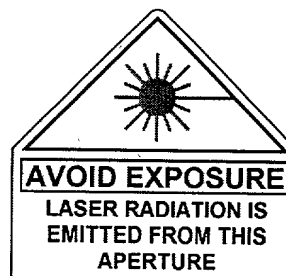
C



D



E



F

FIG. 1
(Prior Art)

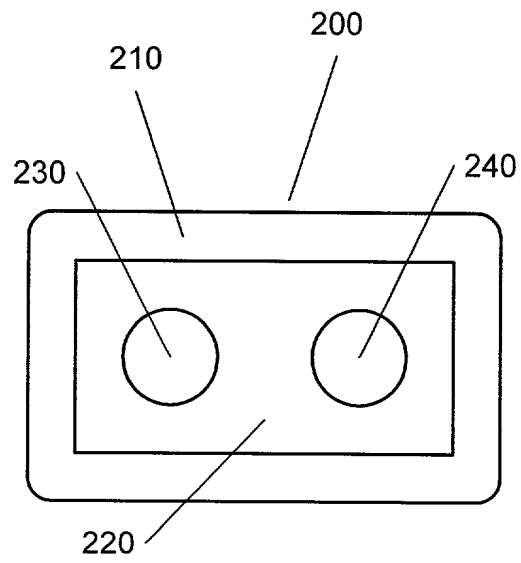


FIG. 2
(Prior Art)

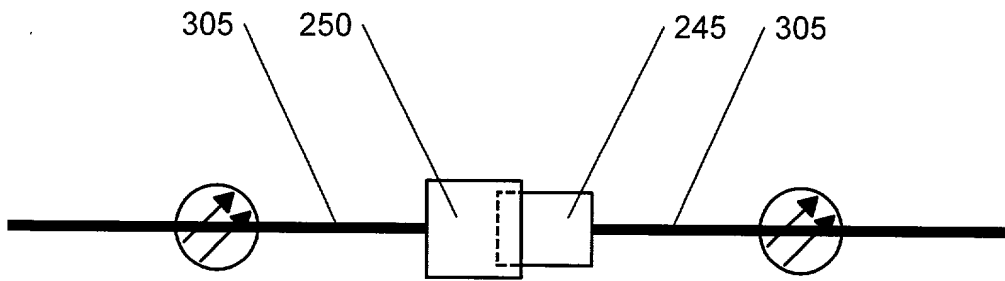


FIG. 3

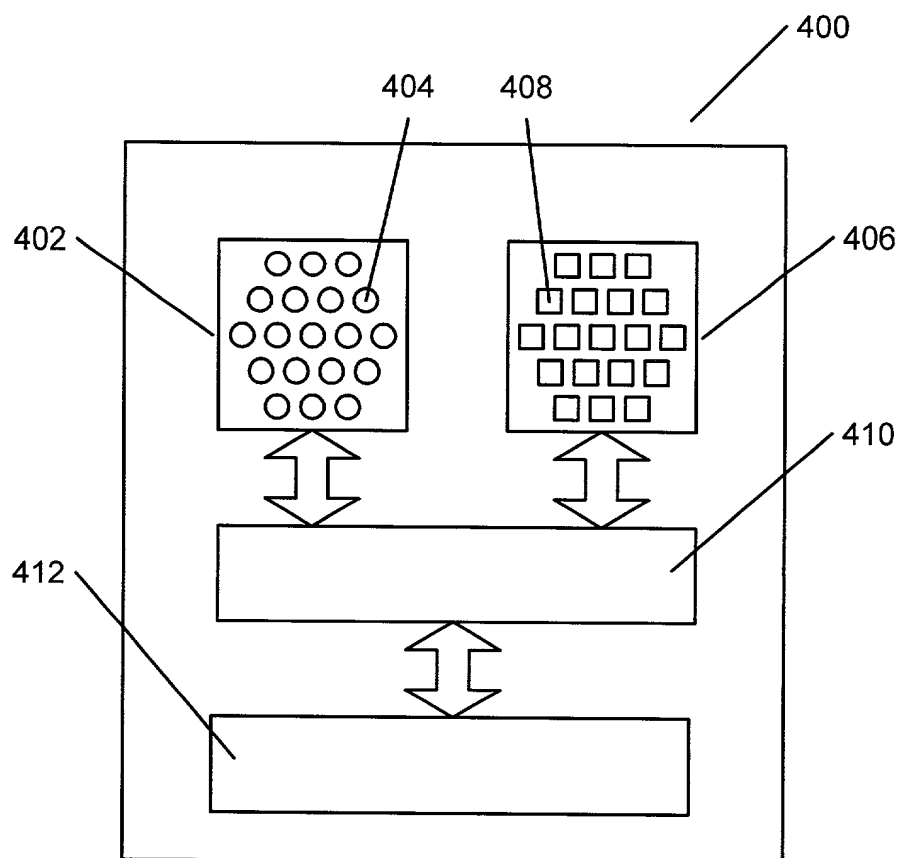


FIG. 4

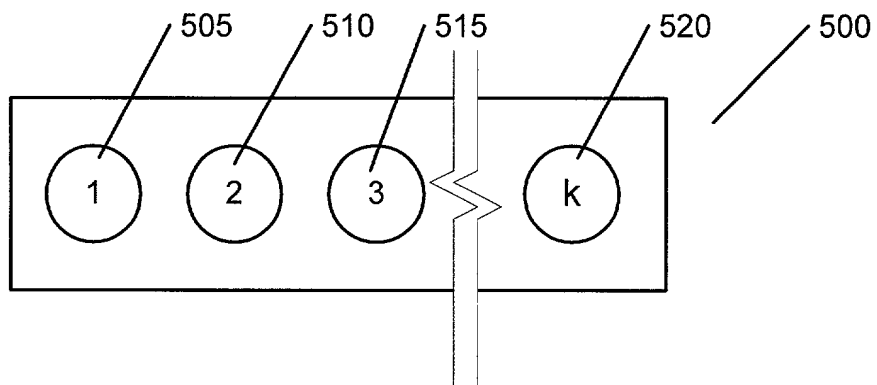


FIG. 5

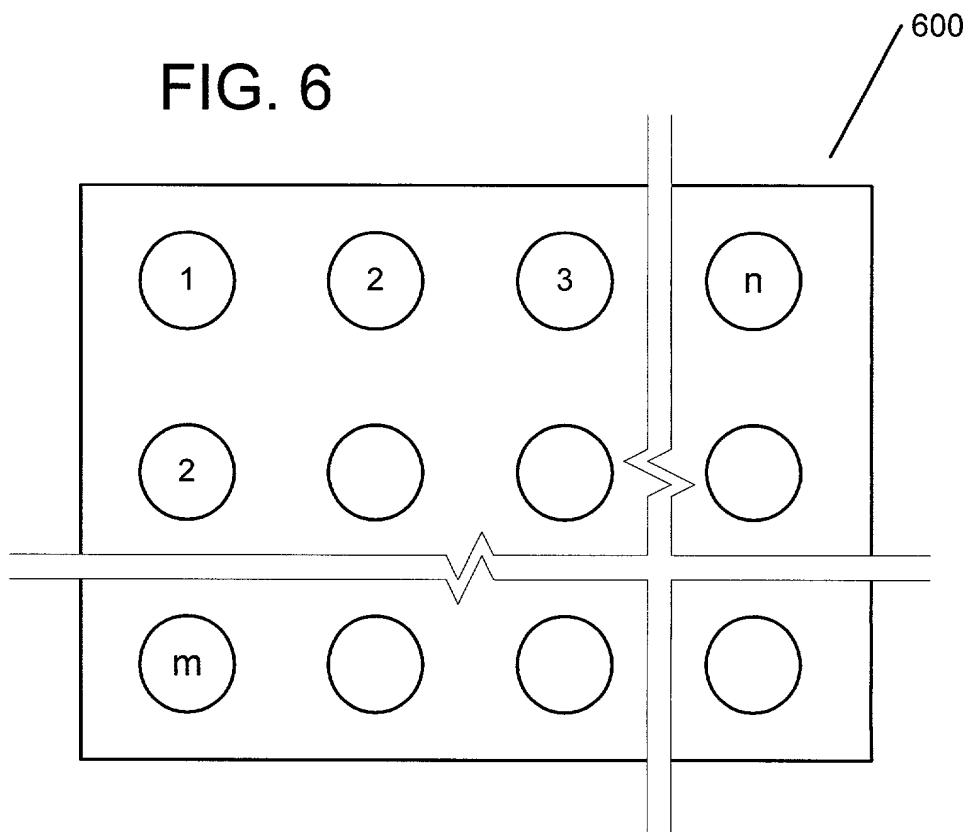


FIG. 6

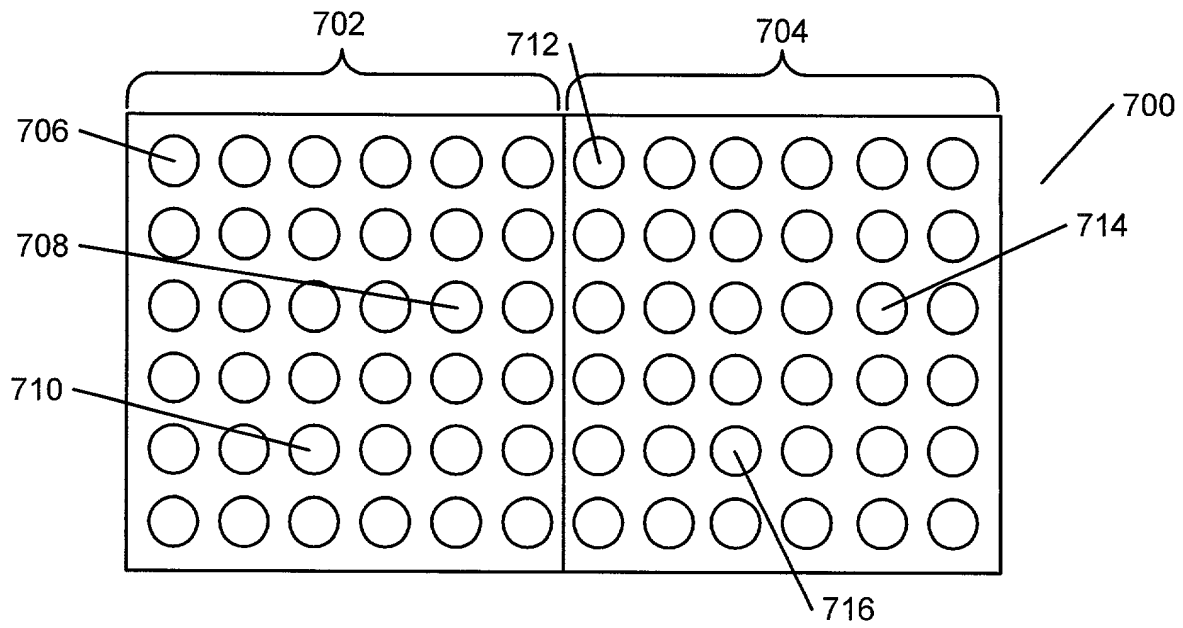


FIG. 7

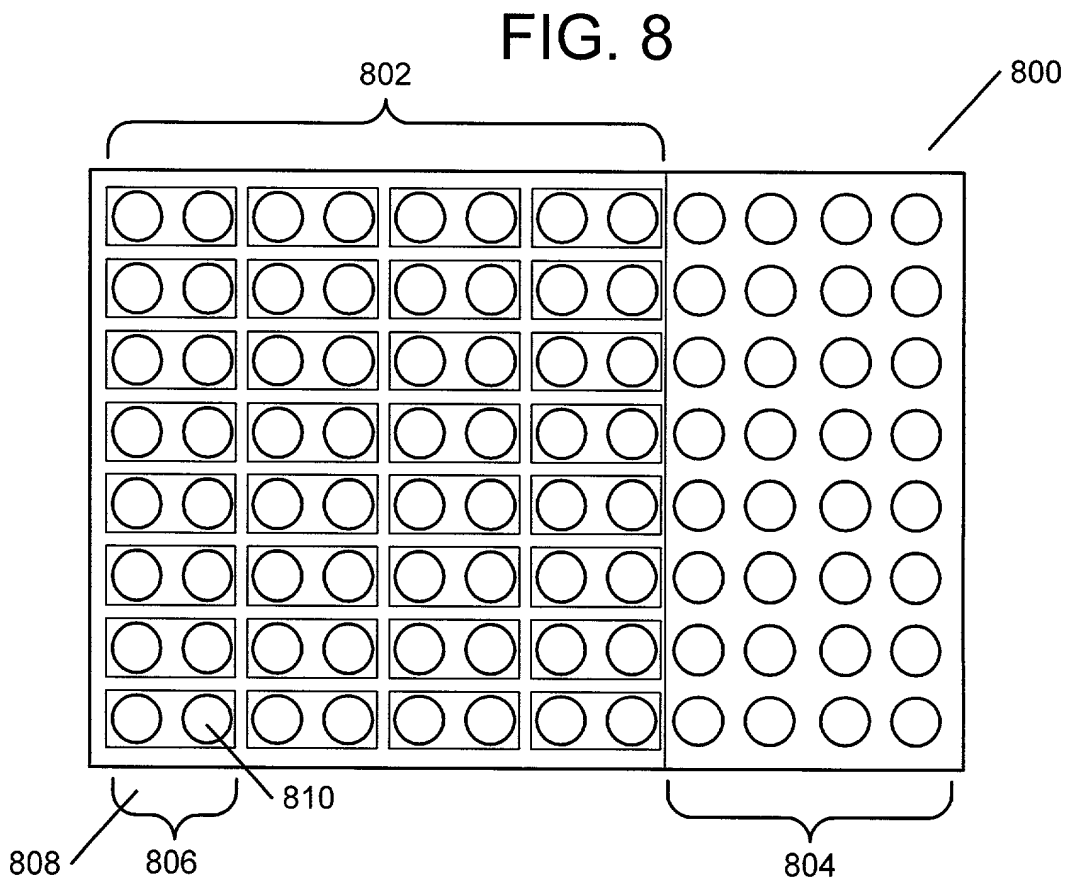


FIG. 8

FIG. 9 is a schematic diagram of a circuit 900, which includes a preamplifier 902, a filter 904, a differential amplifier 910, and a biasing circuit 906. The circuit 900 is configured to process an input signal and generate a differential output signal.

900

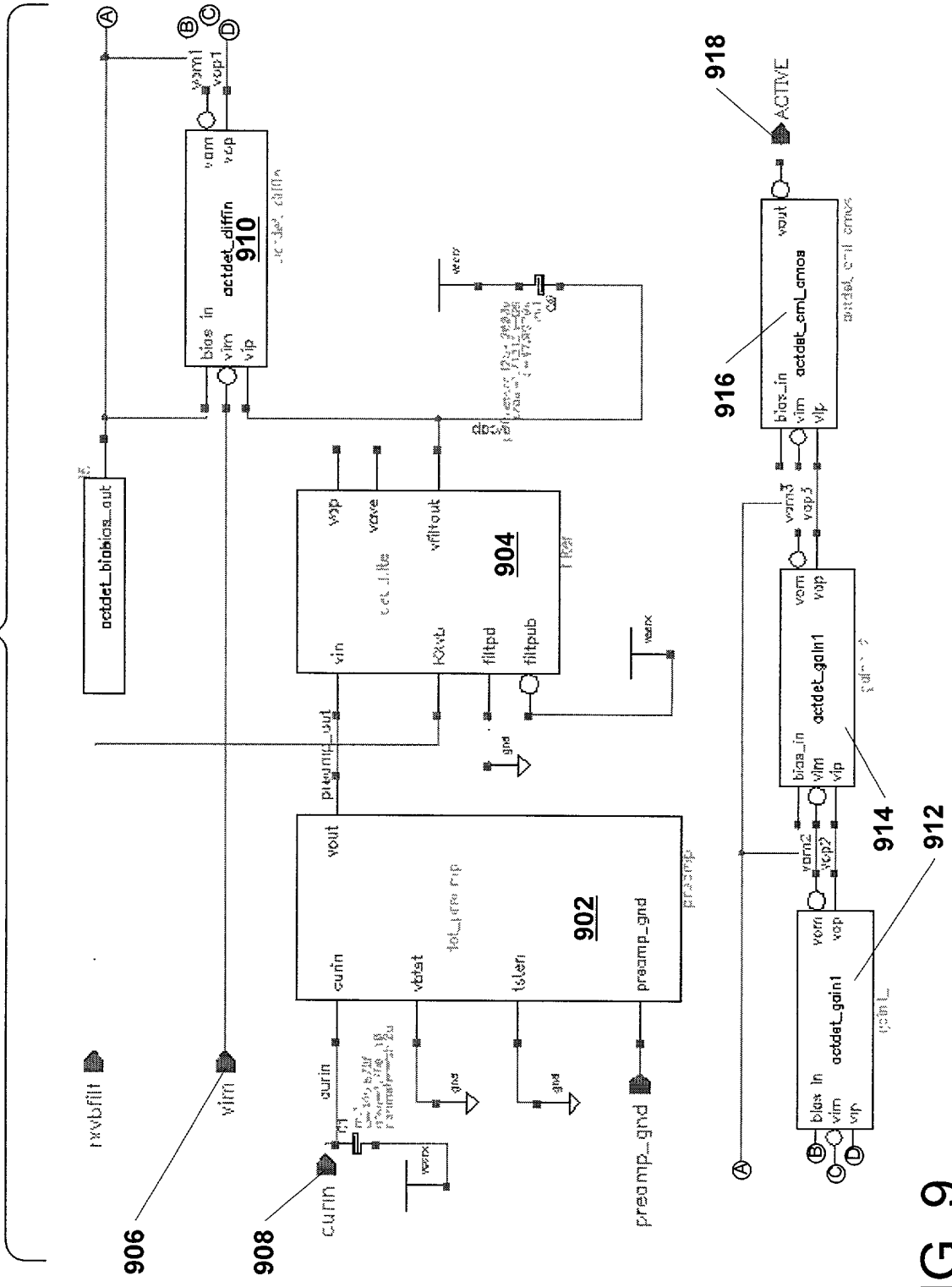


FIG. 9

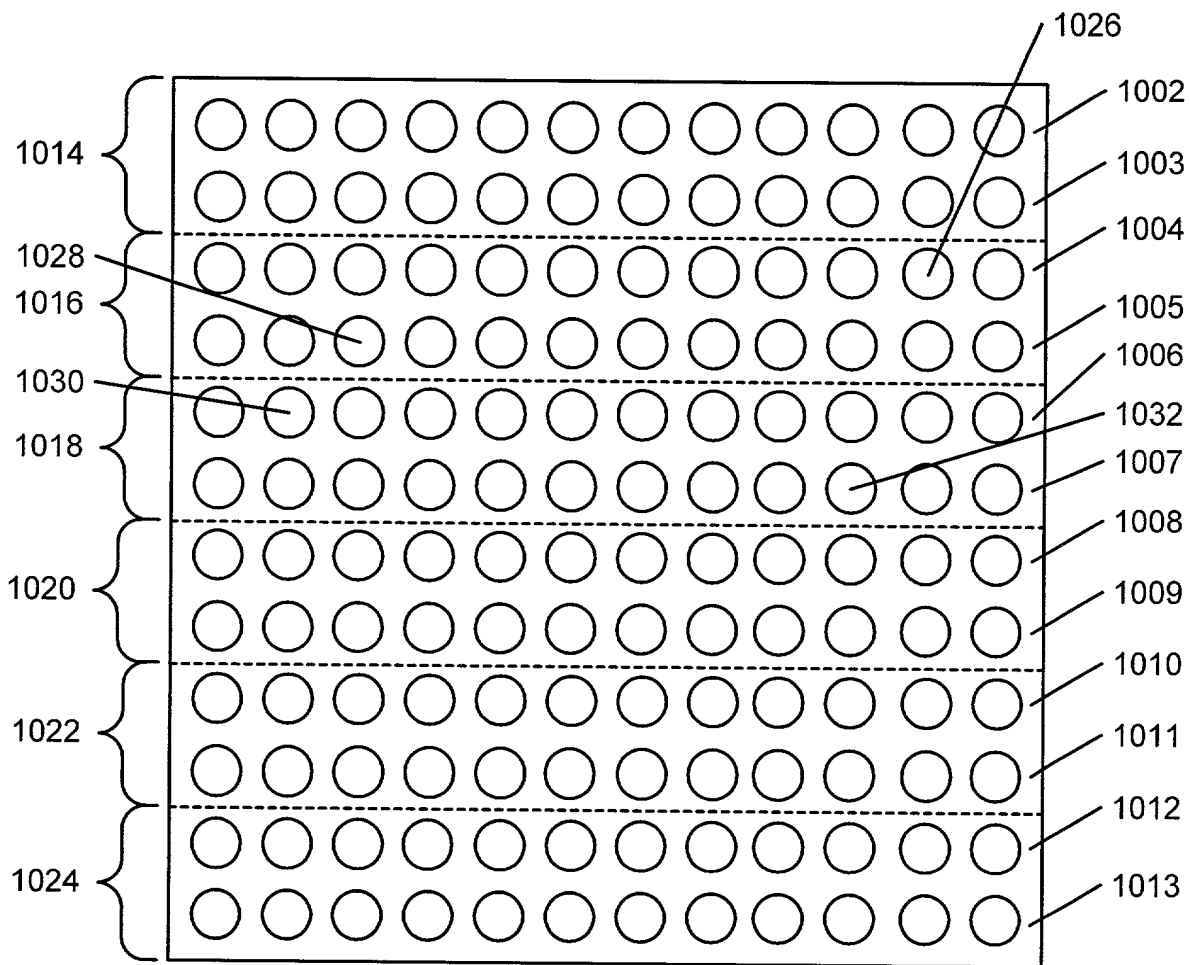


FIG. 10